

1071

Semiconductors

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Silicon compatible materials and integrated devices for photonics and optical sensing

This symposium will focus on innovative materials and devices integrated on Si platform with the main objective to bring together people involved on this topic but working in different application fields from telecommunication to sensing.

Scope:

The extensive and ever advancing miniaturization in microelectronics imposes higher and higher barriers. To master these challenges, more and more materials with highly specific properties have already been and still have to be integrated on the Si technology platform. However, this high rate of innovation does not only drive the development of microelectronics, but also creates new emerging application fields. This especially applies to integrated Si-based photonics which has an enormous bandwidth of potential applications ranging from telecommunication to optical sensing. Several classes of Si compatible materials have been explored for light emitters, amplifiers and detectors, also their coupling with plasmonic materials permits to manipulate light at the nanoscale on the Si platform. The same integrated photonic devices are recently also devoted to the emerging field of on-chip biological and chemical sensing by allowing ultra-high sensing performance and efficient CMOS-compatible readout schemes.

This symposium intends to highlight the newest developments and breakthroughs in terms of materials and their integration for photonic purposes, integrated device design and architecture, as well as advanced and innovative applications. Many topics are highly interdisciplinary and settled at the interface between optics, electronics, material science, chemistry and biology. Thus, this symposium will provide a discussion forum which brings scientists and engineers from these areas together and stimulates an exchange between academia and industry.

Hot topics to be covered by the symposium:

The symposium will include, but will not exclusively be limited, to the following hot topics.

Materials science with related integration techniques:

- Si nanostructures like clusters and nanowires
- rare earth based materials
- compound semiconductor and Ge integration for light emission and detection
- C-based materials
- plasmonic materials and metamaterials

The sessions will include also the following devices and application areas:

- light emitters and detectors
- modulators, optical switches
- resonators, photonic crystals, plasmonic sensors
- integrated waveguide sensing
- building blocks for telecommunication

List of invited speakers (confirmed):

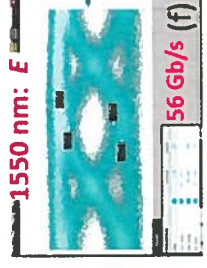
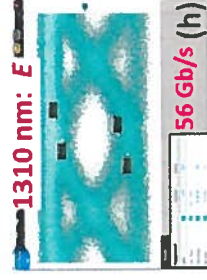
- Ryan Bailey (University of Illinois, Urbana, USA)
- Silke Christiansen (Helmholtz-Center for Materials and Energy Berlin -HZB-, Germany) "*Three-dimensional silicon based nano-architectures for energy conversion and sensing*"
- Philippe Fauchet (Vanderbilt University, USA) "*Photonic crystals for sensors*"
- Ewold Verhagen (FOM Institute, The Netherlands) "*Nano-optomechanical sensing with subwavelength light fields*"
- Romain Quidant (ICFO - The Institute of Photonic Sciences, Spain) "*Nanoplasmonics for biosensing*"
- Gunther Roelkens (University of Ghent, Belgium) "*III-V on silicon photonic integrated circuits for optical communication and sensing*"
- Ioannis Raptis (Institute of Nanosciences & Nanotechnologies (INN) NCSR 'Demokritos', Greece)
- Markus Schmidt (IPHT Jena, Germany) "*Hybrid fibers: a new base for plasmonic nanoprobe and optofluidic nanoparticle sensing*"
- Pol Van Dorpe (IMEC, Belgium)
- Ralf B. Wehrspohn (Fraunhofer Institute, Germany) "*Stable field-enhanced emission and surface ionization from silicon nano-tip arrays*"

List of scientific committee:

- Katerina Dohnalova - University of Amsterdam, The Netherlands
- Blas Garrido - Universitat de Barcelona, Spain
- Peter Masher - McMaster University, Canada
- Daniel Navarro Urrios - CNR-NEST, Italy
- Alexei Nazarov - NAS Ukraine, Kiev

III-V-on-silicon photonics for optical interconnect applications

Germanium photodetectors



56Gbit/s operation both in C-band and O-band (-1 V bias)

Responsivity: 0.7 A/W (C-band) and 0.9 A/W (O-band)

[Chen, OFC 2016]

Heterogeneous III-V-on-Silicon Photonic Integrated Circuits for Communication and Sensing Applications

Gunther Roelkens

Photonics Research Group, Ghent University / imec - Belgium

European Material Research Society Spring meeting 2016

Silicon Photonics:
Using 200mm/300mm CMOS fabrication infrastructure for PIC realization
Very compact PICs because of high index-contrast
Low cost in high volume
Integration with electronics
Do creative design in a process that can instantly go into commercialization

Figure: IBM

Lasers for optical interconnect applications

What if we go to 400GbE or 1.6TbE transceivers ?

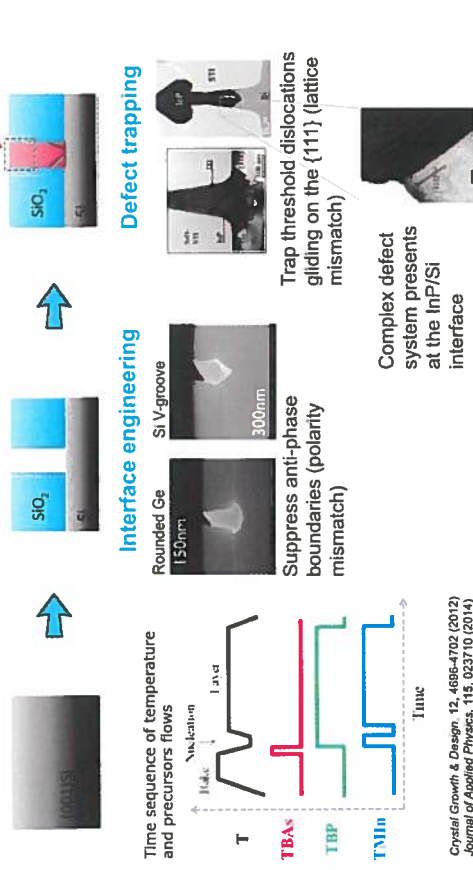
- 1 fiber x 56G x 8 lasers
- 1 fiber x 56G x 32 lasers
- 4 fibers x 56G x 2 lasers
- 4 fibers x 56G x 8 lasers
- 4 fibers x 56GB PAM-4 x 1 laser
- 4 fibers x 56GB PAM-4 x 4 lasers

need a scalable approach for laser integration on Si Photonic ICs

- More lasers on a single chip
- High throughput manufacturing
- Lower cost per laser on the chip

III-V on silicon integration is an enabling technology

Laser integration: hetero-epitaxy



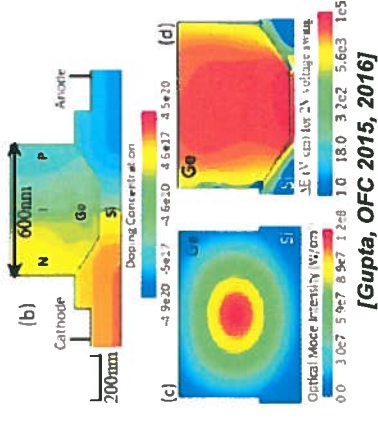
Ge electroabsorption modulators

- Use Franz-Keldysh effect in Germanium
- Shift operation to 1550nm by adding 1% Si

50GHz EO bandwidth
5dB insertion loss
12fJ/bit dynamic PC



56 Gbit/s @ 1550 nm wavelength



Lasers for optical interconnect applications

100 Gbps off-board transceivers: 4 x 28Gbps parallel

- one single laser per transceiver
- no hermetic sealing of full transceiver: laser needs hermetic sealing
- Pick-and-place assembly



Silicon Photonic die

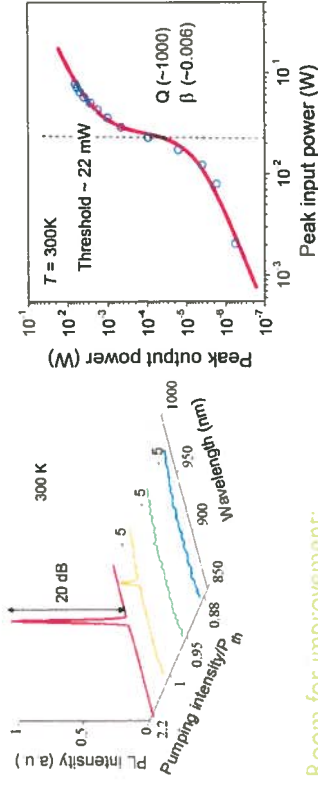


MEMS Laser Source

Source: Luxtera

Laser integration: hetero-epitaxy

Pumping condition:
532 nm wavelength
9 ns pulse duration



Room for improvement:

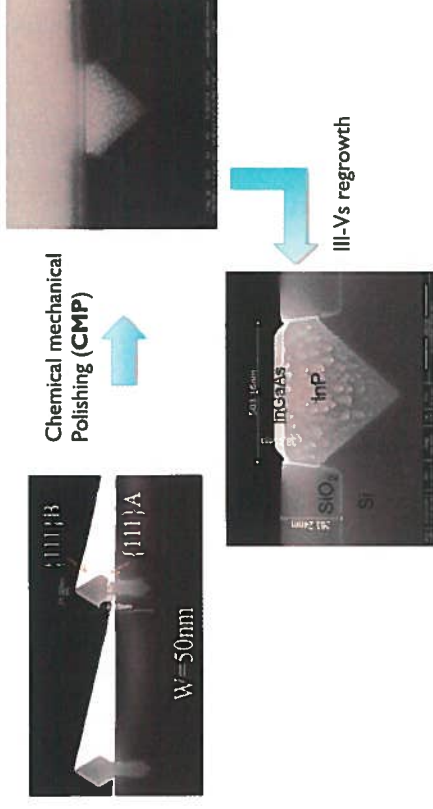
- Use narrow waveguide
- better material
- Heterostructure
- reduced carrier loss

Below threshold

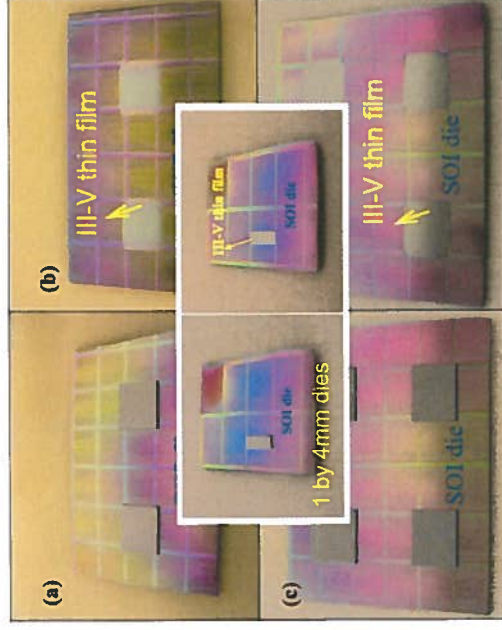
Output

Above threshold

Laser integration: hetero-epitaxy

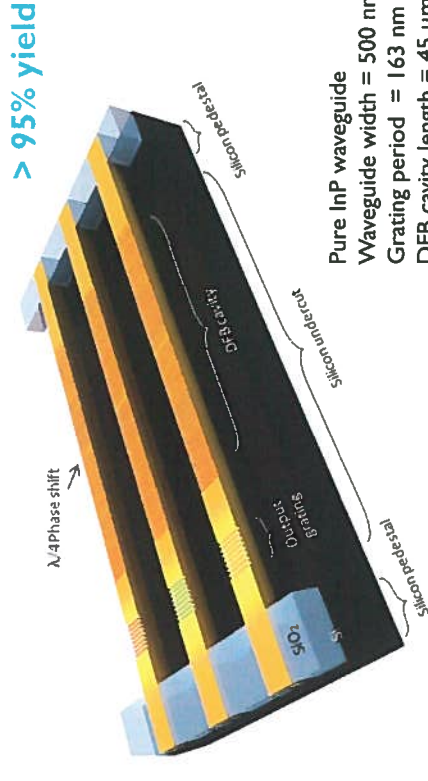


III-V integration on SOI



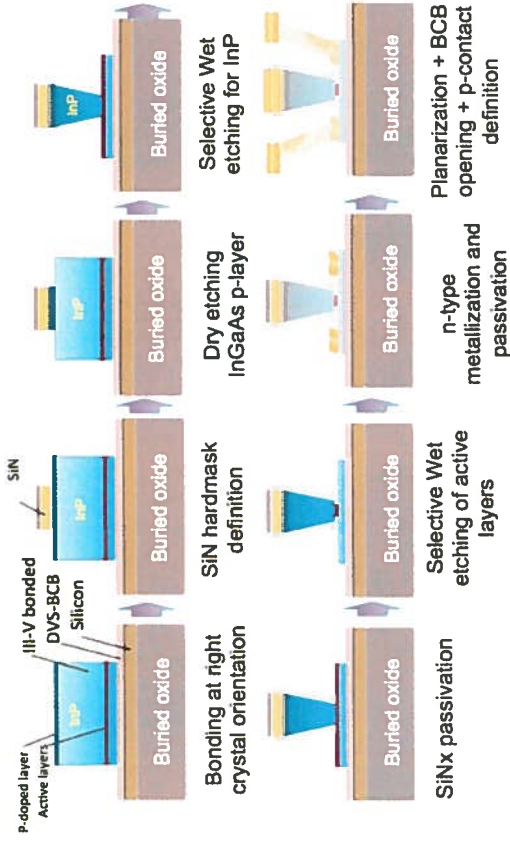
Laser integration: hetero-epitaxy

Schematic plot of the monolithic InP lasers on silicon

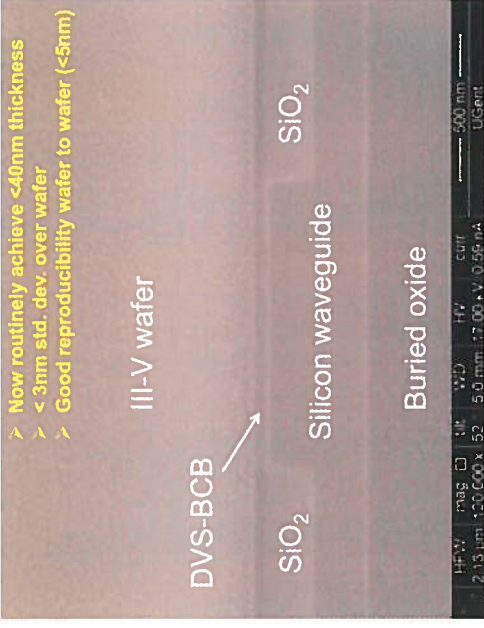


Pure InP waveguide
Waveguide width = 500 nm
Grating period = 163 nm
DFB cavity length = 45 μ m

Process flow



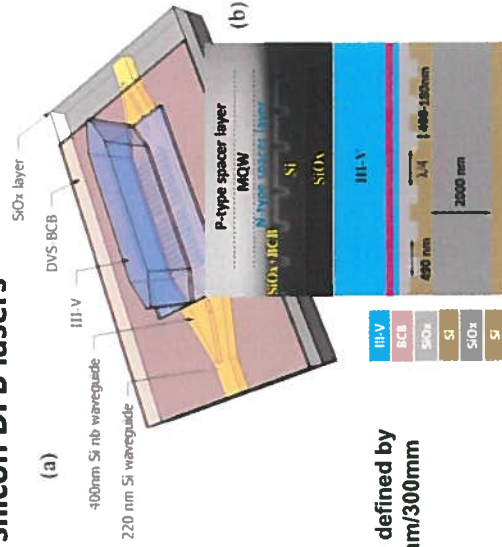
III-V integration on SOI



- > Now routinely achieve <40nm thickness
- > < 3nm std. dev. over wafer
- > Good reproducibility wafer to wafer (<5nm)

[Keyvaninia, Optical Materials Express 2013]

III-V-on-silicon DFB lasers

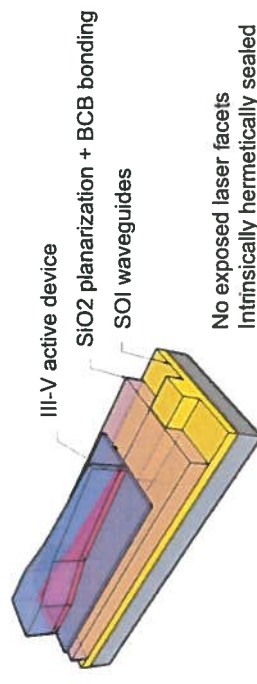


DFB gratings defined by DUV on 200mm/300mm

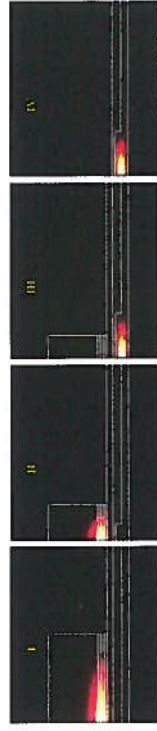
[Keyvaninia, Optics Letters 2013]

Lasers for optical interconnect applications

From full confinement in III-V to full confinement in SOI



No exposed laser facets
Intrinsically hermetically sealed



Fundamental mode in different cross-sections (BCB thickness=80nm)

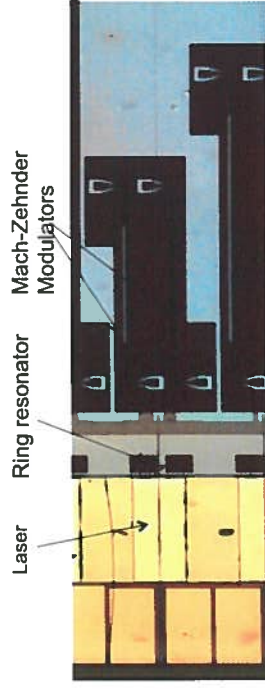
III-V-on-silicon extended cavity tunable laser

[Duan, ECOC 2012]

[Keyvaninia, Opt. Express 2013]

III-V/silicon tunable laser realized

- 8nm tuning range, based on thermo-optic tuning of silicon ring resonator
- >40dB SMSR
- threshold of 35mA
- 4mW optical output power, 1.7MHz linewidth
- co-integrated with silicon modulator



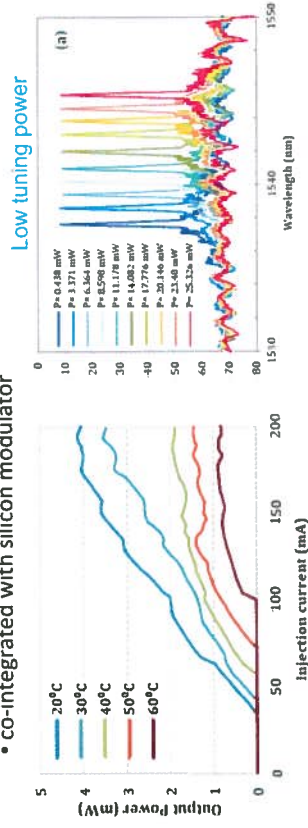
III-V-on-silicon extended cavity tunable laser

[Duan, ECOC 2012]

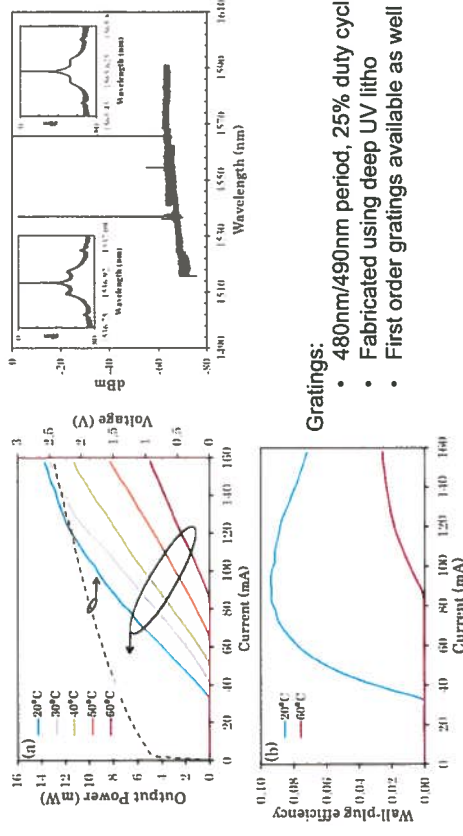
[Keyvaninia, Opt. Express 2013]

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III-V-on-silicon DFB lasers – static characteristics

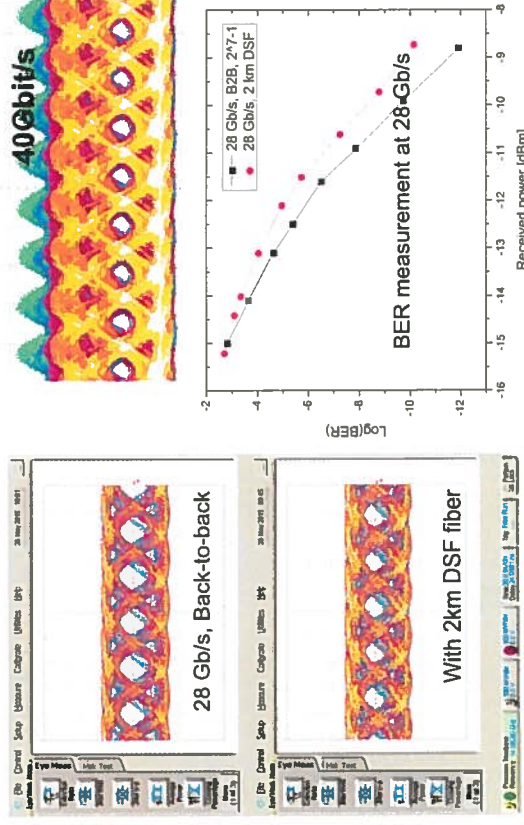


Gratings:

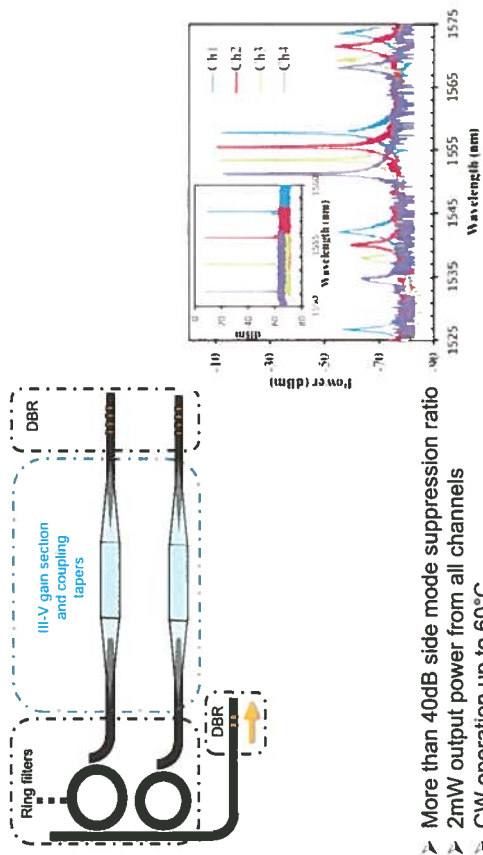
- 480nm/490nm period, 25% duty cycle
- Fabricated using deep UV litho
- First order gratings available as well

DFB lasers – direct modulation

[Abassi, Optics Express 2015]
[Abassi, OFC 2016]

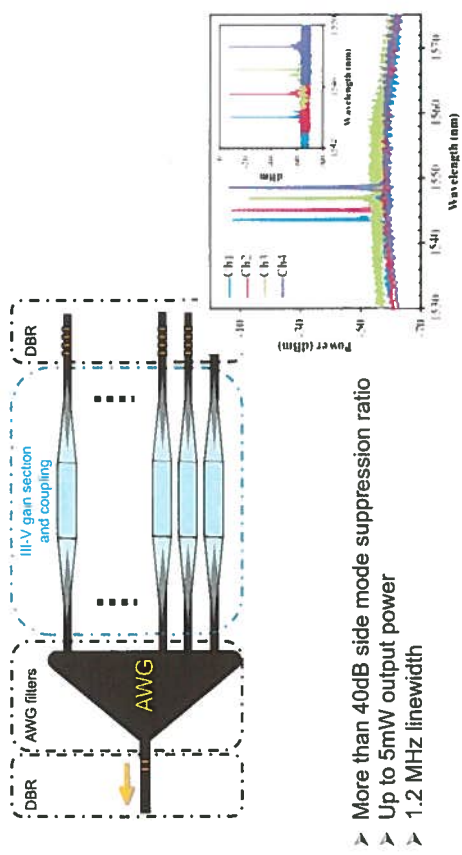


III-V-on-silicon multi-wavelength laser sources



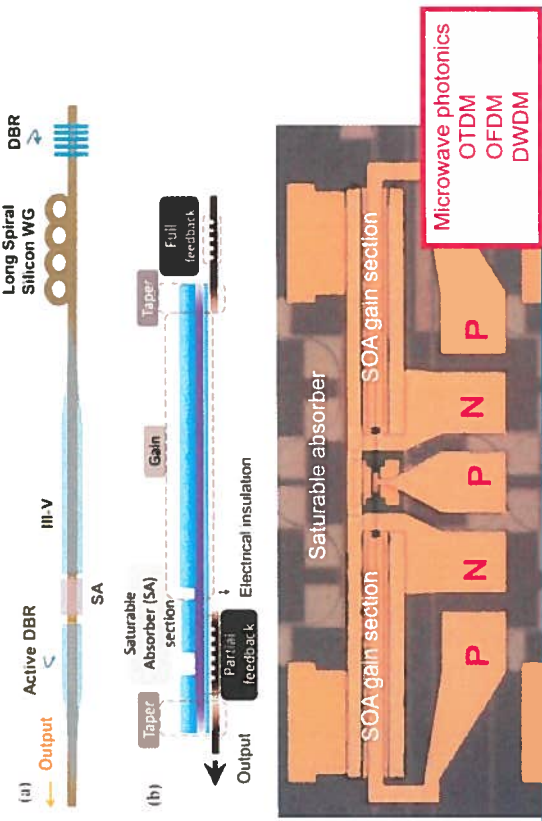
Keyvanina et al., ACP PDP 2012

III-V-on-silicon multi-wavelength laser sources

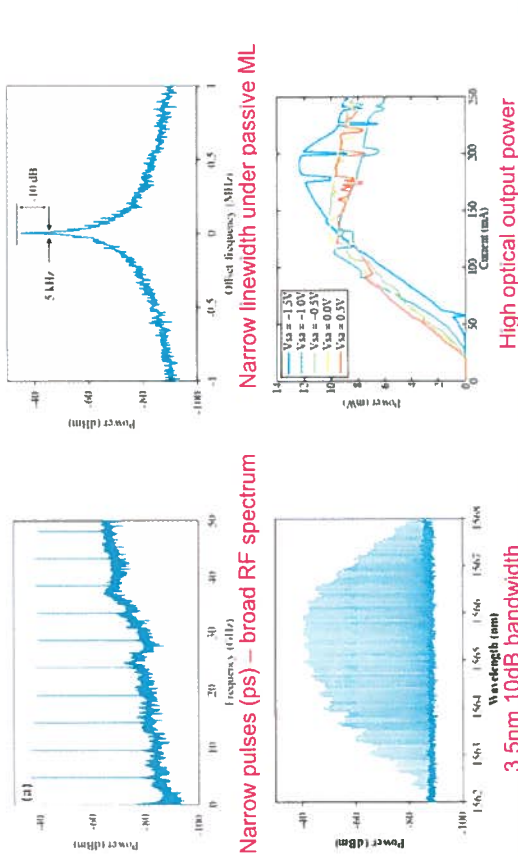


Keyvanina et al., ACP PDP 2012

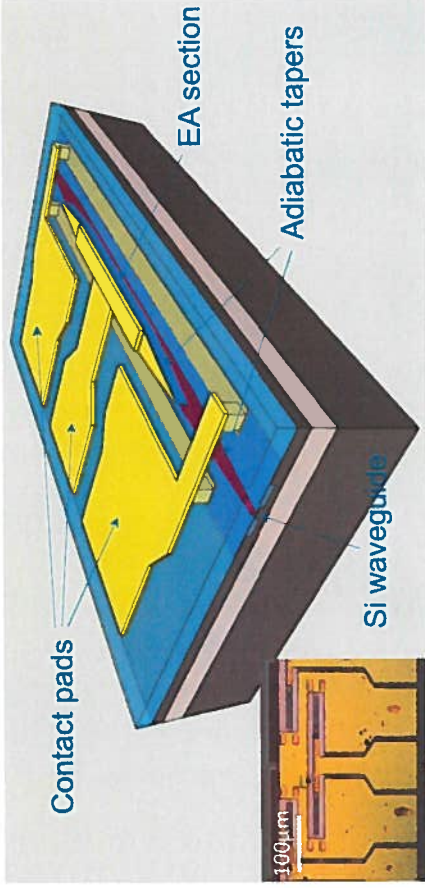
Anti-colliding pulse type modelocked lasers



Modelocked laser performance - ACPMLL



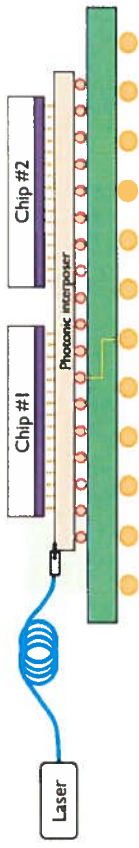
III-V-on-silicon EAM based photonic interposer



Photonic interposers

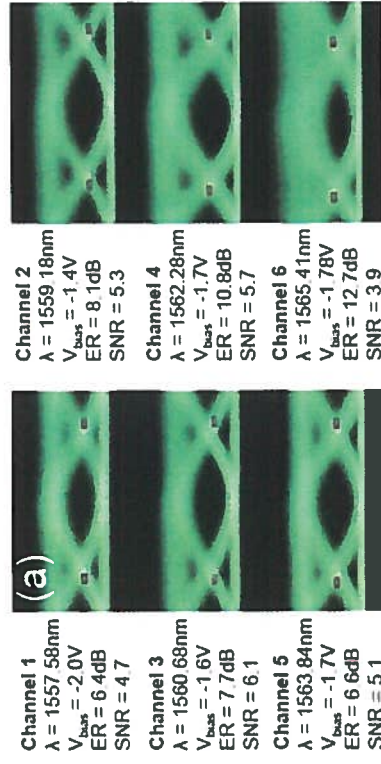
Previous device demonstrations geared towards off-board or off-chip communications

In the future there will also be a need for photonic interposers



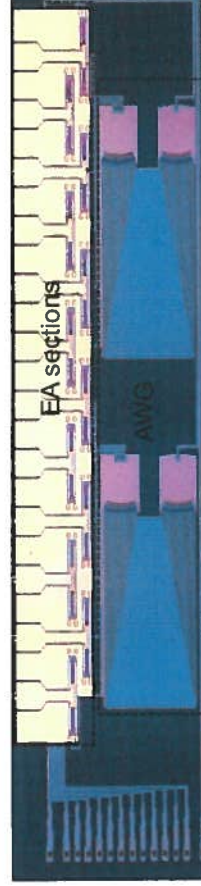
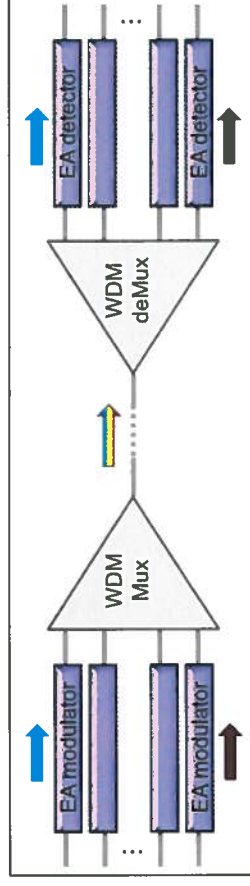
Package I/O Requirements	20-28nm	14nm	10nm	7nm	5nm
CMOS tech node	20-28nm	14nm	10nm	7nm	5nm
I/O Bandwidth	2.5Tbps	5Tbps	10Tbps	25Tbps	50Tbps
I/O Energy	1 fJ/bit	5pJ/bit	1 fJ/bit	0.1-1pJ/bit	50-200fJ/bit
Channel Rate	up to 35Gbps	up to 50Gbps	up to 70Gbps	up to 100Gbps	up to 140Gbps
Cost Target	\$55/Tbps	\$55/Tbps	\$55/Tbps	\$5/Tbps	\$/Tbps
Optical I/O Distance & Appl.	5m to 20m Network	5m to 20m Backplane	5cm to 20m Board	5m to 20m Interposer	1mm to 2cm Chip

III-V-on-silicon EAM based photonic interposer

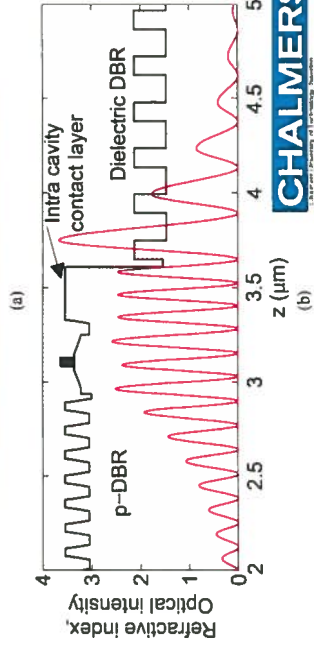


6 x 30Gb/s

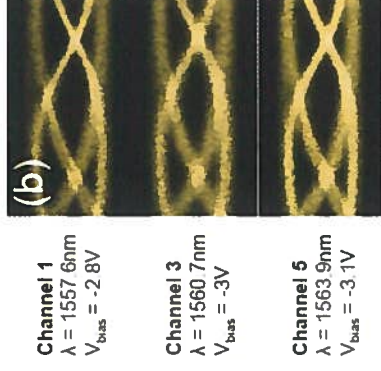
III-V-on-silicon EAM based photonic interposer



850nm GaAs VCSEL photonic interposers



III-V-on-silicon EAM based photonic interposer



Channel 1
 $\lambda = 1557.6\text{nm}$
 $V_{\text{bias}} = -2.8\text{V}$

Channel 3
 $\lambda = 1560.7\text{nm}$
 $V_{\text{bias}} = -3\text{V}$

Channel 5
 $\lambda = 1563.9\text{nm}$
 $V_{\text{bias}} = -3.1\text{V}$

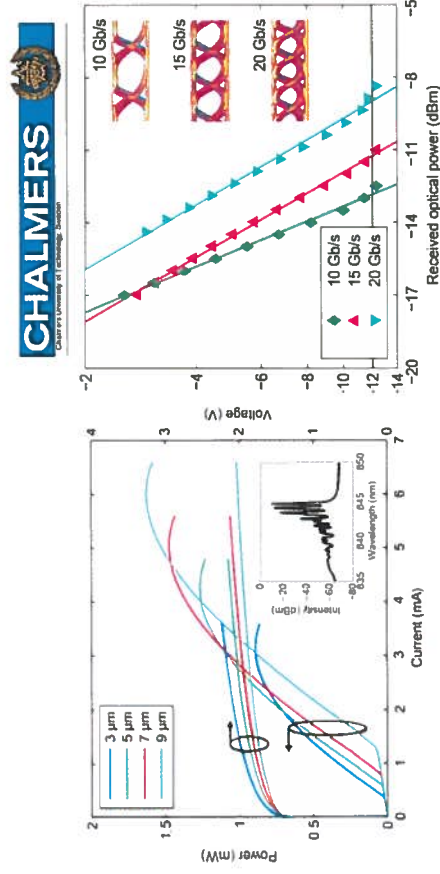
Channel 2
 $\lambda = 1559.1\text{nm}$
 $V_{\text{bias}} = -3.3\text{V}$

Channel 4
 $\lambda = 1562.3\text{nm}$
 $V_{\text{bias}} = -3.3\text{V}$

Channel 6
 $\lambda = 1565.5\text{nm}$
 $V_{\text{bias}} = -3.1\text{V}$

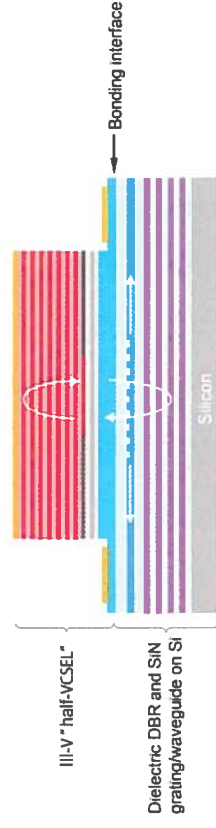
6 x 30Gb/s

850nm GaAs VCSEL photonic interposers



[Haglund, Optics Express 2015]
[Haglund, Photonics Technology Letters 2015]

850nm GaAs VCSEL photonic interposers



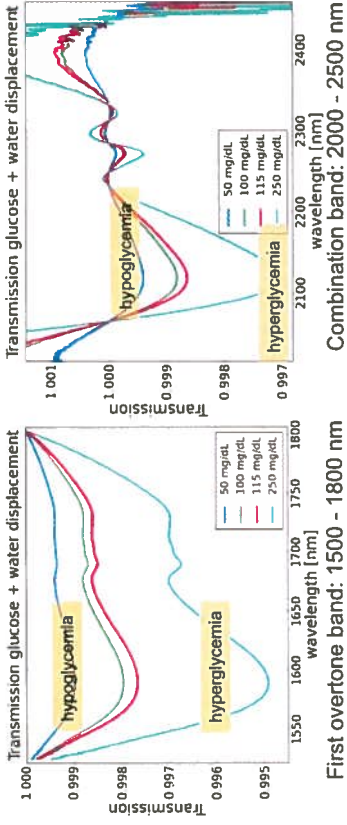
Integrate 850nm VCSELs through water bonding on a SiN-on-insulator waveguide platform

Implement dielectric DBR on silicon substrate

Implement intra-cavity outcoupling grating

Direct modulation

Measurement of glucose concentration (diabetes)



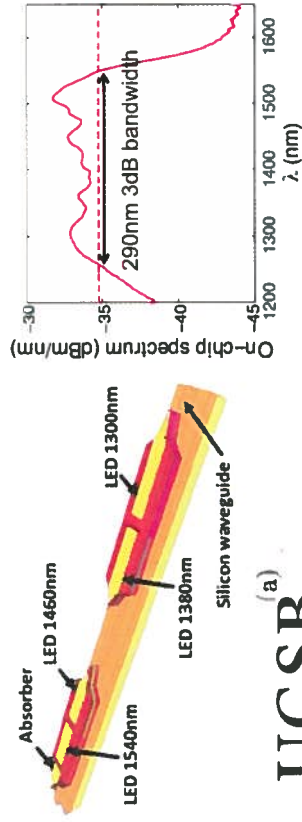
Need for broadband light sources and integrated spectrometers
Wavelength range of interest: 1.2 – 2.5µm

III-V-on-silicon photonics for optical sensing applications

Broadband waveguide coupled LEDs

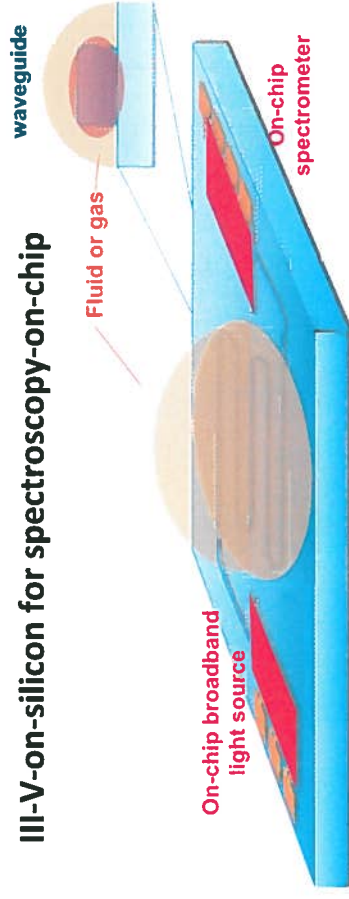
Problem with bulk/quantum well III-V stacks: limited bandwidth

- Solution 1: multiple die-to-wafer bonding + quantum well intermixing to extend the wavelength range



[De Groote, *Optics Letters* 2014]

III-V-on-silicon for spectroscopy-on-chip

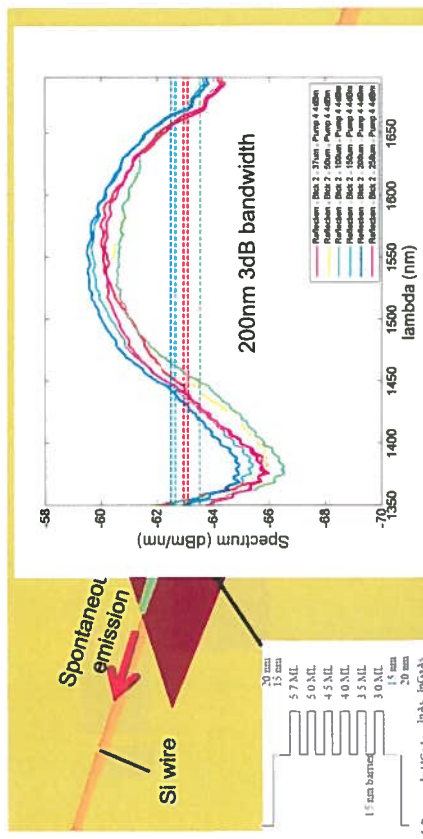


Spectroscopy-on-chip: use the characteristic absorption features of molecules to detect their presence and concentration

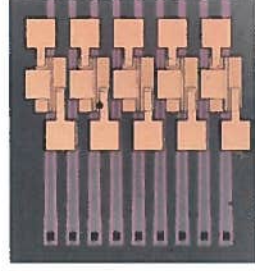
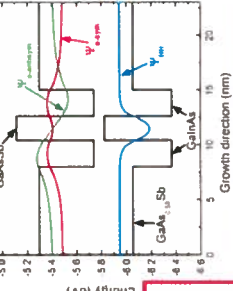
Broadband waveguide coupled LEDs

Problem with bulk/quantum well III-V stacks: limited bandwidth

- Solution 2: use chirped quantum dot waveguide material



InP-based type II short-wave infrared photodetectors



Layer	Material	Thickness
P-contact	InGaAs	100 nm
P-cladding	P-InP	1.5 μm
SCH	P-AlGaAsSb	250 nm
Barrier	GaAs _{0.55} Sb _{0.42}	9nm*7
MQW	Ga _{0.32} In _{0.68} As	2.6 nm*6
	GaAs _{0.33} Sb _{0.67}	2.9 nm*6
	Ga _{0.32} In _{0.68} As	2.6 nm*6
SCH	GaAsSb	130 nm
N-contact	N-InP	200 nm

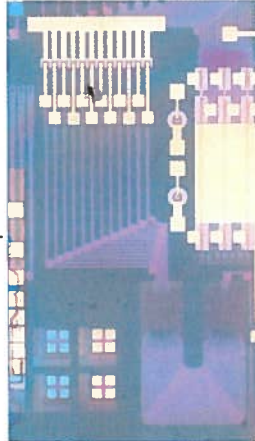
12nA dark current at -0.5V
2.4um cut-off wavelength
On-chip responsivity > 1A/W

[R. Wang, Optics Express 2015]

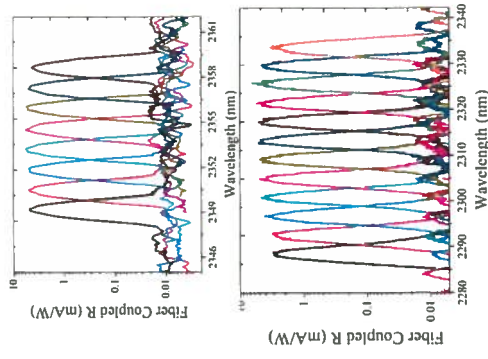
InP-based type II short-wave infrared photodetectors



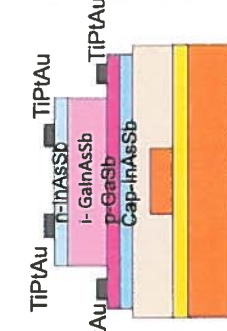
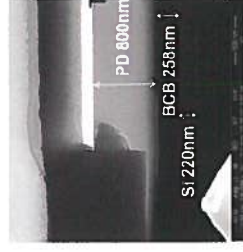
8 channel 2.3-2.4 um wavelength high resolution AWG spectrometer



12 channel 2.3-2.4 um wavelength low resolution AWG spectrometer



GaSb-based short-wave infrared photodetectors



- Dark current: 1μA at room temperature
- Responsivity 0.4 A/W at room temperature (2μm-2.5μm)
- Detector footprint: 50μmx20μm



[N. Hattasan, Photonics Technology Letters 2012]

Conclusions

III-V on silicon integration is maturing

- improving performance
- improving yield and process control

First applications in silicon photonic transceivers

- WDM laser arrays on silicon
- Mode-locked lasers on silicon
- EAMs on silicon
- GaAs VCSELs on silicon

Other applications in the field of optical spectroscopic sensing