

# InP Nanowire lasers Epitaxially Grown on (001) Silicon ‘V-groove’ templates

Bin Tian<sup>1</sup>, Zhechao Wang<sup>1</sup>, Marianna Pantouvaki<sup>2</sup>, Weiming Guo<sup>2</sup>, Joris Van Campenhout<sup>2</sup>, Merckling Clement<sup>2</sup>, Dries Van Thourhout<sup>1</sup>

<sup>1</sup>: INTEC Department, Ghent University, Sint-Pietersnieuwstraat 41, Ghent 9000, Belgium

<sup>2</sup>: IMEC, Kapeldreef 75, 3001 Heverlee, Belgium

e-mail: bin.tian@intec.ugent.be

**Abstract**—We demonstrate an ultra-low threshold nanowire laser monolithically integrated on a (001) silicon substrate. By using a V-groove template we were able to reduce the laser threshold by one order of magnitude (0.19pJ per pulse) compared with our earlier devices and dramatically increased the yield throughout the wafer.

## I. INTRODUCTION

Silicon photonics has been proven to be an outstanding platform for telecom and datacom since it was introduced nearly a decade ago and is now also investigated for its potential for bio-sensing and non-linear applications. Efficient light emission directly from silicon has not yet been demonstrated and therefore typically III-V based devices are integrated wherever an on-board light source is required. Methods based on flip-chip [1], wafer bonding [2-4] and transfer printing [5] have been investigated for almost a decade now, but despite many successes, still suffer from poor heat sinking, a non-standard process flow and the corresponding performance and cost limitations. Therefore, to achieve compact integrated lasers with better performance (good heat dissipation, lower power consumption, and lower cost) monolithic integration based on selective area epitaxial growth was proposed. However, InP, typically used for telecom band devices, has a 8.1% lattice mismatch with silicon. Several solutions have been proposed recently to avoid the threading dislocations and anti-phase boundaries (APB) originating from this mismatch [6-8]. Previously we reported a polytypic InP nano-cavity laser epitaxially grown on (001) Silicon [9] using a step-surface-Germanium seed layer to eliminate the anti-phase boundaries (APB) [10]. In that case however, only a very limited fraction of the devices formed the desired nanowire shape and exhibited laser operation. Here we present improved yielding of the nanowire growth and material quality by using ‘V-groove’ templates [11], which improve the nucleation of InP in trenches [12]. APBs can only form at the trench corners and are blocked by burying the corner before heteroepitaxy.

## II. FABRICATION

To prepare the V-groove templates, first 40nm by 200nm rectangular openings were defined on 300nm-thick oxide layer on a (001) silicon wafer using a standard shallow trench isolation (STI) process. Next V-grooves were formed at the bottom of the STI-trenches in a wet etching process (5% TMAH solution at 80°C). Prior to the epitaxial growth, the

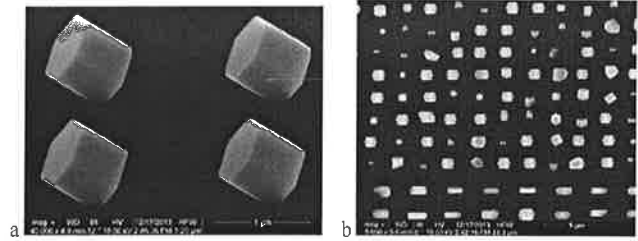


Figure 1. a) Tilted SEM picture demonstrating pillars with similar shapes and dimensions. b) An SEM top view of a random location from the sample. Note that the bottom two rows had a different mask opening size.

wafer was etched by HF and baked in H<sub>2</sub> at high temperature for 10 minutes to remove the native oxide layer. During the cooling down, tertiarybutylarsine (TBAs) was introduced to passivate the {111}Si surface. The main growth is performed in a two steps metal-organic vapor phase epitaxy process (MOVPE). First a low temperature growth forms a stable InP nucleation layer, then a high temperature process results in an epilayer with high crystalline quality. Figure 1(a) shows a typical scanning electron microscopy image of the InP nanowires grown on top of the template. The hexagonal outer shape and the uniform tilted angle with respect to the sample surface prove that these are typical InP nanowires inclining to the [111] crystalline direction. Fig.1 (b) shows a SEM image of the sample surface. Around 35 out of 80 sites exhibit the desired nanowire shape, which is an improvement by up to two orders of magnitude compared to our earlier work [9] using Ge-based seeds. These results hold for most parts of the wafer.

## III. CHARACTERIZATION RESULTS

Optical characterization of the InP-on-Si nanowires was performed on a micro-photoluminescence (PL) setup using a  $\times 50$ , 0.6 numerical aperture objective both to deliver the pump light and to collect the emitted light. The PL signal was resolved by a 1/4 m monochromator with a TE-cooled silicon detector. For wide area PL measurements the diameter of the pumping area was around 20 $\mu$ m, while for single laser characterization the pumping spot was reduced to 3 $\mu$ m.

APB free III-V material grown on a Si(111) surface was demonstrated by several groups before but twins and stacking faults are inevitable in most cases [7-8]. The PL spectrum of a single pillar under CW pumping using a 532nm Nd:YAG laser (dashed line in Fig. 2(a)) shows that the peak is slightly blue

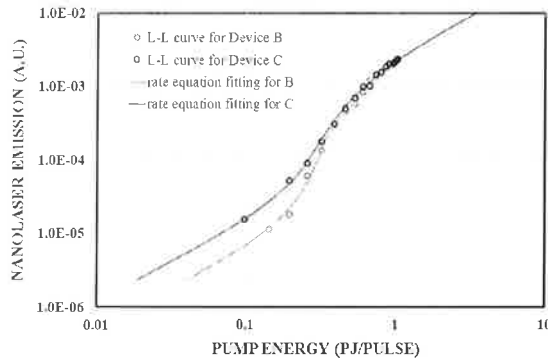
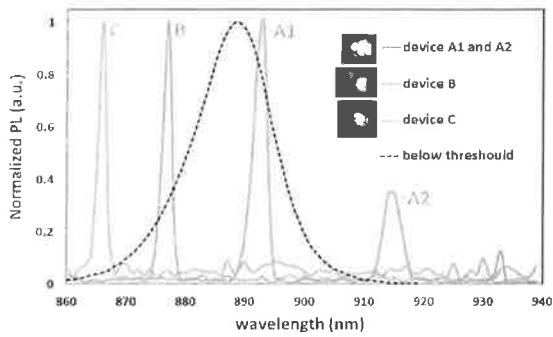


Figure 2. a) Normalized PL of different devices (solid colored lines) above threshold under pulsed pumping. The blue line originates from two devices, A1 and A2, too close to be resolved separately. The black dashed line shows the normalized PL below threshold (CW pumping). b) L-L curves of devices B and C at room temperature under pulsed pumping. Circles denote the measured points. From a rate equation fitting (solid lines) a threshold of 0.21pJ and 0.19pJ was extracted for device B and C respectively.

shifted from the PL of pure Zinblende InP at room temperature [13]. As discussed in our previous work, this effect mainly comes from the polytypic crystal property of the nanowires: super-lattice like heterostructures are formed by packing of Zinblende and Wurtzite InP crystal phases, and the corresponding quantum confinement blue shifts the bandgap. Under pulsed optical pumping using a Nd:YAG 532 nm laser with 7ns pulses at 259Hz repetition rate, lasing is obtained for a large fraction of the InP-on-Si nanowires.

Fig. 2(a) shows the normalized PL spectra of different nanowire lasers above threshold, and the insets are the corresponding PL images. Also in terms of laser yield we find

A dramatic increase using the ‘V-groove’ template compared to our earlier results. A PL image of a random area on the sample, as shown in Fig. 3, shows densely spaced working nanowires within the 20 $\mu$ m-diameter pumped area.

#### IV. DISCUSSIONS AND CONCLUSION

As shown previously by FDTD simulations [9], the optical modes that are supported by these tilted nanowires are helically propagating modes. Although these cavities are much shorter compared with other nanowire lasers, optical modes with Q-factors up to 150 are supported. Thanks to their limited size only a few modes exist in the wavelength range of the gain spectrum, resulting in a large spontaneous emission factor ( $\beta$ )

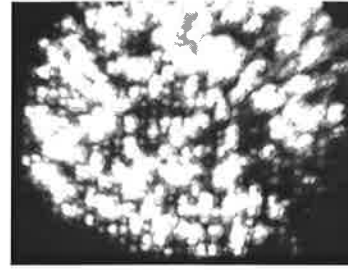


Figure 3. The PL image pumped by pulsed laser, and the diameter of the pumping area is around 20 $\mu$ m.

(we extracted a value  $\beta = 0.06-0.075$  from the results shown in Fig 2b). This high  $\beta$  is essential for laser threshold reduction.

In summary, we improved the growth yield and reduced the threshold of InP nanowire lasers compared to our earlier work by switching to ‘V-groove’ templates. The device becomes much simpler to fabricate, more robust and promising for applications, such as bio-sensing and optical quantum communication.

#### REFERENCES

- [1] Edge, C.; Ash, R.M.; Jones, C.G.; Goodwin, M.J., "Flip-chip solder bond mounting of laser diodes," *Electronics Letters*, vol.27, no.6, pp.499,501, 14 March 1991
- [2] H.Wada and T. Kamijoh, "Room-temperature CW operation of InGaAsP lasers on Si fabricated by wafer-bonding," *IEEE Photonics Technology Letters*, 8, 173-175 (1996).
- [3] A.W. Fang, H. Park, O. Cohen, R. Jones, M.J. Paniccia, J.E. Bowers "Electrically pumped hybrid AlGaInAs-silicon evanescent laser," *Optics Express*, 14, 9203-9210 (2006).
- [4] P.R. Romeo, J. Van Campenhout, P. Regreny, A. Kazmierczak, C. Seassal, X. Letartre, G. Hollinger, D. Van Thourhout, R. Baets, J.M. Fedeli, L. Di Cioccio, "InP on Silicon Electrically Driven Microdisk Lasers for Photonic ICs," *Optics Express*, 14, 3864-3871 (2006).
- [5] J. Justice, C. Bower, M. Meitl, M. B. Mooney, M. A. Gubbins, B. Corbett, Wafer-scale integration of group III-V lasers on silicon using transfer printing of epitaxial layers, *Nature Photonics* 6, 610-614 (2012)
- [6] Wang, Z.; Junesand, C.; Metaferia, W.; Hu, C.; Wosinski, L. Lourdudoss, S. *Mater. Sci. Eng. B* 2012, 177 (17), 1551 – 1557.
- [7] J. Motohisa, J. Noborisaka, J. Takeda, M. Inari, T. Fukui, 2004 Catalyst-free selective-area MOVPE of semiconductor nanowires on (111)B oriented substrates. *J. Cryst. Growth*, 272, 180-185, 0022-0248.
- [8] F. Ren, K. Wei Ng, K. Li, H. Sun, Chang-Hasnain, Connie J., High-quality InP nanoneedles grown on silicon, *Applied Physics Letters*, 102, 012115 (2013),
- [9] Z. Wang, B. Tian, M. Paladugu, M. Pantouvaki, N. Le Thomas, C. Merckling, W. Guo, J. Dekoster, J. Van Campenhout, P. Absil, D. Van Thourhout, Polytypic InP Nano-laser Monolithically Integrated on (001) Silicon, *Nano Letters*, 2013, 13, 5063-5069.
- [10] C. Merckling, N. Waldron, S. Jiang, W. Guo, O. Richard, B. Douhard, A. Moussa, D. Vanhaeren, H. Bender, N. Collaert, M. Heyns, A. Thean, M. Caymax, and W. Vandervorst, *J. Appl. Phys.* 114, 033708 (2013).
- [11] M. Paladugu, C. Merckling, R. Loo, O. Richard, H. Bender, J. Dekoster, etc., Site Selective Integration of III-V Materials on Si for Nanoscale Logic and Photonic Devices, *Cryst. Growth Des.*, 2012, 12 (10), pp 4696-4702.
- [12] C. Merckling, N. Waldron, S. Jiang, W. Guo, N. Collaert, M. Caymax, E. Vancoille, K. Barla, A. Thean, M. Heyns & W. Vandervorst, "Heteroepitaxy of InP on Si(001) by Selective-Area Metal Organic
- [13] Mishra, A. and Titova, L. V. and Hoang, T. B. and Jackson, H. E. and Smith, L. M. and Yarrison-Rice, J. M. and Kim, Y. and etc., *Applied Physics Letters*, 91, 263104 (2007)



**ISCS 2014**

The 41<sup>th</sup> International Symposium on Compound Semiconductors



**IPRM 2014**

The 26<sup>th</sup> International Conference on Indium Phosphide and Related Materials

**2014**  
**11<sup>th</sup> - 15<sup>th</sup>**  
**May 2014**  
**Le Corum**  
**Montpellier**  
**FRANCE**

**Compound**  
**Semiconductor**  
**Week**

Institut d'Electronique, Université Montpellier 2, UMR CNRS 5214, Montpellier (France)  
 Laboratoire de Photonique et de Nanostructures, CNRS, Marcoussis (France)

Compound Semiconductor Week - 11<sup>th</sup> - 15<sup>th</sup> May 2014 - Le Corum - Montpellier - FRANCE

Day	Time	Activity
Sunday, 11 May	8h30	Opening
	9h	Penary 1
Monday, 12 May	9h30	Penary 2
	10h30	coffee break 10h15-10h45
	11h	Penary 3
	11h30	Penary 4
	12h30	Lunch - 12h30-13h30
	13h	Lunch - 13h-14h00
	13h30	Mo-A1 IPRM Mo-B1 IPRM Mo-C1 IPRM Mo-D1 IPRM
	14h	Tu-A3 IPRM Tu-B3 IPRM Tu-C3 IPRM Tu-D3 IPRM
	14h30	course A
	15h	Break
Tuesday, 13 May	15h30	Mo-A2 IPRM Mo-B2 IPRM Mo-C2 IPRM Mo-D2 IPRM
	16h	coffee break 15h-15h30
	16h30	course B
	17h	Registration
	17h30	Poster session/beers/wines - 17h-19h
	18h30	Registration
	19h	Conference dinner 19h-22h
	20h30	Registration
	21h	Registration
	21h30	Registration
Wednesday, 14 May	8h30	We-A1 IPRM We-B1 IPRM We-C1 IPRM We-D1 IPRM
	9h	Physic-Frequency
	9h30	High-Frequency
	10h	Photodiode
	10h30	coffee break 10h30-11h
	11h	We-A2 IPRM We-B2 IPRM We-C2 IPRM We-D2 IPRM
	11h30	Photodiode
	12h30	Excursion 13h-18h (lunch is provided)
	13h	Excursion 13h-18h (lunch is provided)
	13h30	Excursion 13h-18h (lunch is provided)
Thursday, 15 May	8h30	Th-A1 IPRM Th-B1 IPRM Th-C1 IPRM Th-D1 IPRM
	9h	Photodiode
	9h30	Photodiode
	10h	coffee break 10h30-11h
	11h	Th-A2 IPRM Th-B2 IPRM Th-C2 IPRM Th-D2 IPRM
	11h30	Photodiode
	12h30	Closing and Students Award ceremony 12h45-13h15
	13h	Closing and Students Award ceremony 12h45-13h15
	13h30	Closing and Students Award ceremony 12h45-13h15
	13h30	Closing and Students Award ceremony 12h45-13h15

